

Claims

- 5 1. A packet switched backplane having a plurality of
node slots wherein at least two of said node slots
comply with the PICMG 2.16 node standard
characterized in that at least one of said node
slots complying with said PICMG 2.16 node standard
is connected by means of dedicated links to at
10 least one aggregation slot, said aggregation slot
comprising at least one other node slot complying
with said PICMG 2.16 node standard.
- 15 2. The backplane according to claim 1 wherein Ethernet
transmit pins of said node slots are connected to
Ethernet receive pins of said aggregation slot and
Ethernet receive pins of said node slots are
connected to Ethernet transmit pins of said
aggregation slot.
- 20 3. The backplane according to claim 1 wherein at least
one of said node slots is connected to two of said
aggregation slots.
- 25 4. The backplane according to claim 1 having at least
one fabric slot that comply with PICMG 2.16
standard.
- 30 5. A data processing system comprising a packet
switched backplane having a plurality of node slots
wherein at least two of said node slots comply with
the PICMG 2.16 standard and a plurality of node
cards connected to said node slots **characterized in**
that dedicated links connect Ethernet transmit pins
35 of at least one of said node slots to Ethernet

receive pins of at least one aggregation slot, said aggregation slot comprising at least one other node slot as well as connect Ethernet receive pins of at least one of said node slots to Ethernet transmit pins of said aggregation slot to make a direct point-to-point Ethernet connection wherein an aggregation card comprising a node card equipped with an Ethernet bridging unit and an external Ethernet connector is connected to said aggregation slot and said Ethernet bridging unit bridges between said node cards and external addresses by means of said external Ethernet connector.

6. The data processing system according to claim 5 wherein said Ethernet bridging unit is an Ethernet switch.
7. The data processing system according to claim 5 wherein each of said node cards is connected to two aggregation cards.
8. The data processing system according to claim 5 wherein said aggregation card is a Motorola PVRB series card.
9. The data processing system according to claim 5 wherein said aggregation card is a Motorola PCRB series card.